



REGULAR EXAMINATION TIMETABLE, DECEMBER, 2025

BACHELOR OF TECHNOLOGY (B.Tech.)

SECOND YEAR, SEMESTER-III (For 2025-26 Batch Under Autonomy)

Timings: 2:30 p.m. to 4:30 p.m.

Date: 15th November, 2025

Day & Date	Mechanical Engineering	Electronics and Telecommunication Engg.	Information Technology	Computer Engineering	Computer Science & Engineering (Data Science)	Artificial Intelligence and Data Science	Artificial Intelligence and Machine Learning	Computer Science and Engineering (IOT and Cyber Security with Block Chain Technology)
Monday, 01/12/2025	Fundamentals of Electric Vehicles	Mathematics for AIML	Development Frameworks	Statistics for Data Science	Financial Market and Risk Analysis	Foundations of Business Intelligence and Analytics	Computer Graphics and Virtual Reality	Fundamentals of Data Mining
	Introduction to Robotics and Its Applications	Microelectronics		Principles of Financial Engineering			Foundation of Finance	
Thursday, 04/12/2025	Applied Thermodynamics	Electronics Devices & Circuits	Database Management System	Database Management System	Database Systems	Database Management Systems	-	Database Management Systems
Saturday, 06/12/2025	Manufacturing Processes	-	Operating System	Computer Networks	Computer System Fundamentals	-	Operating Systems	Operating Systems
Tuesday, 16/12/2025	Engineering Materials	Digital System Design	Data Structures	Data Structures	-	Data Structures	Data Structures	Data Structures
Thursday, 18/12/2025	Product Lifecycle Management		-	-	Product Lifecycle Management			-
	Management Information System					-	Management Information System	
	Operations Research							
	Personal Finance Management							
	Entrepreneurship and Startup Ecosystem							
Saturday, 20/12/2025	Mathematics for Mechanical Engineering	Mathematics for Telecommunication Engineering	-	Computational Mathematics	Mathematics for Intelligent Systems	Linear Algebra and Optimization Techniques		
Tuesday, 23/12/2025	Economics and Financial Management		Universal Human Values			Economics and Financial Management		

Important Note: Changes if any, in the time table, shall be communicated on the college website (www.djsce.ac.in)

Ms. Alka Shukla
 Controller of Examinations

Dr. Hari Vasudevan
 Principal